

HP-41C Load and Review Data Registers by John Dearing
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```
01 LBL "LOAD" ;Load Data Registers
02 1.4
03 STO 00
04 FIX 0
05 CF 29
06 LBL 01
07 "R"
08 ARCL 00
09 ">=?"
10 PROMPT
11 STO IND 00
12 ISG 00
13 GTO 01
14 RTN
15 LBL "REVIEW" ;Review Data Registers
16 1.4
17 STO 00
18 LBL 02
19 FIX 0
20 CF 29
21 "R"
22 ARCL 00
23 ">="
24 FIX 2
25 SF 29
26 ARCL IND 00
27 PROMPT
28 ISG 00
29 GTO 02
30 END
```

```
0049C000F5004C4F4144111A14309C00A91D02F1529B00F37F
3D3F8E91809600B20085C000F700524556494557111A143003
9C00A91DF1529B00F27F3D9C02A81D9B808E9600B300C0000D
34
```

73 BYTES

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Program Registers Needed: 11

Row 1 (1 - 4)



Row 2 (4 - 10)



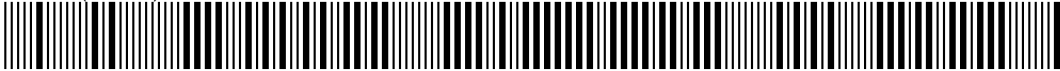
Row 3 (11 - 15)



Row 4 (15 - 20)



Row 5 (21 - 26)



Row 6 (27 - 30)

